

# Ricoh, Japan and Moscape Partner to Build a Cohesive Circuit Design Methodology for System LSI Development.

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SANTA CLARA, Calif.--(BUSINESS WIRE)--April 10, 2000

Moscape, Inc. today announced a long-term partnership with Ricoh Company, Ltd., Tokyo, Japan to develop a cohesive circuit design methodology with comprehensive circuit analysis to enable aggressive design techniques while ensuring first-pass silicon success.

Moscape's flagship product, CircuitScope(TM) with unique assertion-based technology enables designers to uncover several implementation issues to increase quality and performance of their design prior to tapeout.

Moscape has delivered the customizable assertion-based analysis engine that performs circuit integrity analysis for aggressive design techniques such as pass transistor logic and dynamic logic. After a thorough evaluation of the capabilities, Ricoh decided to work with Moscape to enhance the engine to satisfy the specific requirements of their circuit designers.

The design engineering team in the Processor Development Center at Ricoh, Osaka, validated the fact that Moscape's technology addresses a crucial problem in DSM design -- ensuring electrical integrity -- with a very unique solution. The team was also impressed with Moscape's willingness to support Ricoh and to enhance the capabilities of CircuitScope to make it an integral part of their circuit design flow.

"Ricoh is spearheading true system-on-chip designs with aggressive design techniques," said Nitin Deo, vice president of Marketing at Moscape. "They are a natural partner to work with -- extremely cooperative, demanding but reasonable, and willing to spend resources on the right issues. We enjoy working with Ricoh and look forward to many successful system LSI ICs."

## About CircuitScope

CircuitScope is a production-proven tool for exhaustive analysis of multi-million transistor ICs, providing complete coverage of all recognized circuit structures for potential electrical integrity problems, thereby assuring first silicon success. Built on a foundation of the CAART(TM) engine (Customizable Assertion-based Analysis and Repair Technology), it enables designers to uncover structural and electrical integrity issues to increase quality and performance of their design during pre-layout and post-layout phases prior to tapeout.

## About Moscape

Moscape is the leading provider of assertion-based design integrity solutions for deep submicron system-on-chip (SoC) designs. CircuitScope, Moscape's flagship product enables designers to predict and repair circuit integrity and noise integrity problems, thereby significantly reducing time-to-market. Founded in 1997 and headquartered in Santa Clara, Calif., the company is privately held. For more information on the company and its products call 408/982-4280; email [sales@moscape.com](mailto:sales@moscape.com); or via the

World Wide Web at [www.moscape.com](http://www.moscape.com). Seiko Instruments Inc. is the authorized distributor of Moscape's products in Japan. For more information, please contact EDA Sales Department, Seiko Instruments Inc., Tokyo at 03-5626-8270. Moscape is a member of the Cadence Design Systems (NYSE:CDN)(news), Alanza Group Connections(R) and Synopsys (Nasdaq:SNPS)(news), In-Sync(R) partnership programs.

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